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# **Stencil Design Guidelines**

Developed by the Stencil Design Task Group (5-21e) of the Assembly and Joining Processes Committee (5-20) of IPC

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Users of this publication are encouraged to participate in the development of future revisions.

Contact:

IPC  
3000 Lakeside Drive, Suite 309S  
Bannockburn, Illinois  
60015-1249  
Tel 847 615.7100  
Fax 847 615.7105

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# Stencil Design Guidelines

## 1 PURPOSE

This document provides guides for the design and fabrication of stencils for solder paste and surface-mount adhesive. It is intended as a guideline only as much of the content is based on the experience of stencil designers, fabricators and users. Printing performance depends on many different variables and therefore no single set of design rules can be established.

**1.1 Terms and Definitions** All terms and definitions used throughout this handbook are in compliance with IPC-T-50. Definitions denoted with an asterisk (\*) below are reprints from IPC-T-50. Other specific terms and definitions, essential for the discussion of the subject, are provided below.

**1.1.1 \*Aperture** An opening in the stencil foil.

**1.1.2 \*Aspect Ratio** The ratio of the width of the aperture to the thickness of the stencil foil.

**1.1.3 \*Area Ratio** The ratio of the area of aperture opening to the area of aperture walls.

**1.1.4 Border** Peripheral tensioned mesh, either polyester or stainless steel, which keeps the stencil foil flat and taut. The border connects the foil to the frame.

**1.1.5 Contained Paste Transfer Head** A stencil printer head that holds, in a single replaceable component, the squeegee blades and a pressurized chamber filled with solder paste.

**1.1.6 Etch Factor** Etch Factor = Etched Depth/Lateral; Etch in a chemical etching process

**1.1.7 Fiducials** Reference marks on the stencil foil (and other board layers) for aligning the board and the stencil when using a vision system in a printer.

**1.1.8 Fine-Pitch BGA/Chip Scale Package (CSP)** Ball grid array with less than 1 mm [39 mil] pitch. This is also known as Chip Scale Package (CSP) when the package size is no more than 1.2X the area of the original die size.

**1.1.9 \*Fine-Pitch Technology (FPT)** A surface-mount assembly technology with component terminations on centers less than or equal to 0.625 mm [24.61 mil].

**1.1.10 Foil** The sheet used to create the stencil.

**1.1.11 Frame** This may be made of tubular or cast aluminum to which a tensioned mesh (border) is permanently bonded using an adhesive. The foil is bonded to the mesh. Some foils can be mounted into a re-usable tensioning master frame and do not require a mesh border and negate a permanent bonding of the foil to the frame.

**1.1.12 \*Intrusive Soldering** Intrusive soldering may also be known as paste-in-hole, pin-in-hole, or pin-in-paste soldering. This is a process in which the solder paste for the through-hole components is applied using the stencil. The through-hole components are inserted and reflow-soldered together with the surface-mount components.

**1.1.13 \*Land** A portion of a conductive pattern usually used for the connection and/or attachment of components.

**1.1.14 Modification** The process of changing an aperture in size or shape.

**1.1.15 \*Overprinting** The use of stencils with apertures larger than the lands or annular rings on the board.

**1.1.16 \*Pad** See land.

**1.1.17 \*Squeegee** A metal or polymer blade used to wipe a material (ink or solder paste) across a stencil or silk screen to force the material through the openings in the screen or stencil, onto the surface of a printed board or mounting structure. Normally the squeegee is mounted at an angle such that the contacting edge of the squeegee trails behind the print head and the face of the squeegee slopes forward.

**1.1.18 Standard BGA** Ball grid array with 1 mm [39 mil] pitch or larger.

**1.1.19 \*Stencil** A thin sheet of material containing openings to reflect a specific pattern, designed to transfer a paste-like material to a substrate for the purpose of component attachment.

**1.1.20 Step Stencil** A stencil with more than one foil thickness level.

**1.1.21 \*Surface-Mount Technology (SMT)** The electrical connection of components to the surface of a conductive pattern that does not utilize component holes.

**1.1.22 \*Through-Hole Technology (THT)** The electrical connection of components to a conductive pattern by the use of component holes.